

Members

Founded in January 2008, the SiNANO Institute gathers 23 laboratories from 12 European countries.

IUNET (IT)	Grenoble INP (FR)
University of Warwick (UK)	University of Siegen (DE)
FZJ Research Center Jülich (DE)	Université Catholique de Louvain (BE)
NCSR Demokritos (GR)	KTH Royal Institute of Technology (SE)
Tyndall Institute (IE)	IEMN - Univ. Lille (FR)
Newcastle University (UK)	Chalmers University (SE)
University of Glasgow (UK)	Uppsala University (SE)
Institute of Electron Technology (PL)	Warsaw University of Technology (PL)
Teknologian Tutkimuskeskus VTT (FI)	University Rovira i Virgili / University of Granada (ES)
University of Twente (NL)	University of Liverpool (UK)
Institut d'Electronique du Sud (FR)	Catalan Institute of Nanotechnology (ES)
Universitat Autònoma de Barcelona (ES)	

Contacts

The SiNANO Institute office is located in Minatec, one of the top centres of innovation in Micro and Nanotechnologies in Europe.

Further information about the activities and the members of the Institute can be found on Institute's website: www.sinano.eu



SiNANO Institute

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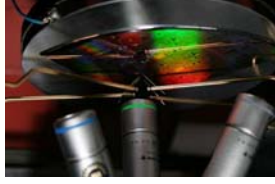
www.sinano.eu

Objectives

The SINANO Institute is a **non profit Association** aiming at establishing a durable EU Network of researchers in order to form a distributed Centre of Excellence in the nanoelectronic field. More than 1 200 scientific and technical staff are working together in this field in the SINANO Institute.

its objectives are to:

- Carry out a role of representation and coordination of the associated Organizations in the area of nanoelectronic devices and technologies,
- Promote the strengthening of the European scientific culture, technical knowledge and skills in the field, and exploit the synergies deriving from the complementary nature of the competencies available at its Members,



- Encourage collaboration between its Members, research bodies and industries,

- Perform training activities, University curricula, Workshops to develop high competence levels in Europe,
- Play an important role in European structuring and programs, and strengthen the overall efficiency of the European research in Nanoelectronics,

- Strong involvement in the ENI² initiative (*European Nanoelectronics Infrastructure for Innovation*)

Competences and facilities

SINANO Institute's members are renowned European research organisations, universities and research centres in the field of nanoelectronics.

Their main competences are in the "**More Moore**", **More than Moore** and "**Beyond-CMOS**" domains:

- Simulation, design, processing, characterisation, modelling and reliability of nanoelectronic devices,
- More Moore: new materials, innovative fabrication steps, advanced device architectures, and their integration into nanoscale CMOS and Memories,
- Beyond-CMOS: top-down and bottom-up processes, nanowires, nanodots, quantum devices, small slope switches, carbon and 2D electronics, etc.

- More than Moore: Micro-nano-bio systems, RF devices, energy harvesters, etc.

The Sinano Institute gathers the most important flexible research infrastructures available in Europe for long term nanoelectronic research.



Activities

The main activities of the Sinano Institute are summarized below:

- **Coordination activities in the frame of European projects.** SINANO facilitates the involvements of its Partners in European projects starting from the Consortium build-up, project proposal preparation and execution (management & dissemination). Noticeable examples are the Projects Nansoil, Guardian Angels, III-V-MOS, NEREID, Nanonets2Sense.

- **Organize Workshops** –for instance the Sinano Workshop during the ESSDERC-ESSCIRC Conference, training activities – for example the Sinano Modelling School and Migas, for disseminating the knowledge.

- **Perform research work** for enhancing device integration, functionality and performance in order to meet the future demands in electronic components and systems,



- **Explore the science and technology aspects** for ultimate technology nodes and novel functionalities for studying and validating new concepts, novel materials and nanotechnologies, innovative device architectures in order to identify the most promising topics for future information technology and speed up technological innovation,

- **Prepare the path for future industrial applications** in the field of communications, computing, consumer electronics, health and environment,